The following instructions are provided to the referees:

1. **Please provide an assessment of the applicant's research excellence and demonstrated leadership.** Include examples to support yourassessment in comparison to the norms particular to the research area(s) (e.g. publication norms for the discipline etc.).

*Criterion: Research excellence and leadership in the research domain* - quality of the applicant's contribution to research and development to date (demonstrated capacity for research excellence) and sphere of influence achieved to date by the applicant (demonstrated capacity for leadership in the research domain).

Mr. Fang is one of themain contributors work onthe research of flicker-free LED driving.Hisresearch is well recognized and solvedthe flicker issue with LED lighting. Mr. Fang demonstrated high productivity research output and had published three first-author papersat the top-level journal and manymore conference papers. He had also filedtwo US/ international patent applications based on his LED driving research. His LED driving method had also received wideacceptancein industry and is evidenced by multiplesuccessful technology transfers. Mr. Fang's work had also been recognized by theresearch community and won him the prize of Research Excellent Award from IEEE KinstonSection.Compared to other Ph.D. students in the U.S and worldwide, Iam more than sure that he is among the very top young power electronics researchers in the field andhas strong potential tobecome a leading researcher in the future.

Mr. Fang had demonstrated his leadership byproactively promoting his research in a variety of conferences and seminars. He took the initiative to introduce his research to the company that can benefit from his research. Hehad also played a key role in collaborating with the industry companies and led multiple technology transfer projects. I am sure he has skills to interact, negotiate with people from the business side of the world. Mr.Fanghad also demonstrated leadership by serving the power electronics community. He is afrequent reviewer for a variety ofjournal and conference papers in the field of power electronics.

1. **Please provide an assessment of merit of the proposed research.** If the research is in an area outside of the applicant’s documented expertise, please speak to the feasibility of the applicant to successfully carry out this research.

*Criterion: Applicant's proposed research program* - potential for the research program, executed in the proposed institutional environment - to position the applicant for significant impact through a research-intensive career (potential for significant impact)

This research proposed by Mr. Fang will have a broad scale impact to power electronic technology. Using GaN device in the power electronics design will lead to revolutionary technology advancement. Unfortunately, gate driving technology for theGaN device is not available at the moment. The research on the gate driving for GaN device will fill the void and unleash the full potential of the GaN device in the power electronics design.

Mr. Fang hasstrong industry and research backgrounds, which allows him to grasp the essence of the research. Hehas aclearvisionof the research problem and knows the steps he needs to find the solution. I am confident that he will produce a successful, high-impact, research output within the time frame.

1. **Please provide an assessment of suitability of the proposed research environment from your perspective.**

Prof. Ng is a renowned scholar and pioneer in the area of in power semiconductor devices, smart power integrated circuits (PIC) design and fabrication technologies. His lab is among the best, worldwide, in the area of integrated power electronics research. Especially, Prof. Ng and his teamhave extensive experience in gate driving research. Prof. Ng can provide the research facility and training that Mr. Fangneeds to achieve a successoutcome for the researchand prepare him to become awell-roundedresearcher in the future.